



Image

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Andrew Marshall, et al. Docket No: TI-31157
Serial No: 10/023,113 Conf. No: 2385
Examiner: Khiem D. Nguyen Art Unit: 2823
Filed: 12/13/2001
For: MEMORY CELL WITH TRANSISTORS HAVING RELATIVELY HIGH THRESHOLD
VOLTAGES IN RESPONSE TO SELECTIVE GATE DOPING

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service with sufficient
postage as First Class Mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA
22313-1450 on 1-6-04.

Ann Trent
Ann Trent

Dear Sir:

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed November 10, 2003, finally rejecting Claims 1-22.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on _____.
☐ is requested for ____ month.
2. ☒ A timely response to the final rejection has been filed.
3. ☒ Fee \$330.00:

☐ Not required (Fee paid in prior appeal)
☒ The Commissioner of Patents is hereby authorized to charge any
fees which may be required or credit any overpayment to the deposit
account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in triplicate.

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-4258

Peter K. McLarty
Peter K. McLarty
Attorney for Appellants
Reg. No. 44,923

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